Name of 1	Faculty:Priyan	Discipline:Computer Engineering		
Semester		Subject:Computer Organization  WORK LOAD (LECTURE/PRACTICAL) PER WEEK (IN HOURS):- LECTURE- 03, PRACTIACL-00		
LESSON	PLAN DURA			
		Theory	Practical	
weeks	Lectures/hrs	Topics(including assisgnments & test)	Experiments	
	1	Introduction of Hardware organisation of computer system		
1	2	CPU organisation: general register organisation,		
	3	stack organisation		
	4	Instruction formats(three address, )		
2	5	Instruction formats(two address, )		
	6	instruction formats(one address, )		
3	7	instruction formats(Zero address, )		
	8	RISC		
	9	Addressing modes: Immediate, register,		
4	10	Addressing modes:Direct,Indirect		
	11	Addressing modes:Relative,Index		
	12	CPU Design: Microprogrammed		
	13	CPU Design : Hired Wired Control		
5	14	Microprogrammed vs hard wired control		
	15	Reduced instruction set computers:		
	16	CISC characteristics, RISC characteristics,		
6	17	Comparison between RISC & CISC		
O	18	Assignment ist		
	19	Sessional Ist		
7				
	20	Memory organisation:Memory Hierarchy		
	21	RAM and ROM chips, Memory address map, Memory connections to CPU.		
8	22	Auxillary memory: Magnetic disks and magnetic tapes.		
	23	Associative memory		
	24	Cache memory		
9	25	Virtual Memory		
	26	Memory Management Hardware		
	27	Read and Write operation		
10	28	Assignment 2nd		
	29	Sessional 2nd		
	30	I/O organisation		
11	31	BIOS and its function		
	32	Testing & initialization		
	33	Configuring the system		
12	34	Data transfer mode		
	35	Programmed I/O:Synchronous		
	36	Programmed I/O:Asynchronous & interrupt Initated		
13	37	DMA Data transfer		
	38	Introduction of Architecture of Multiprocessor system		
	39	Forms of parallel processing		
14	40	Parallel processing and pipelines, basic characteristics of multiprocessor		
	41	General purpose multiprocessors'		
	71	Interconnection networks: time shared common bus, multi port		
	42	memory,		
15	43	cross bar switch, multi stage switching networks and hyper cube structures.		
	44	assignment 3rd		
	45	Test 3rd		